Unit: mm

TOSHIBA Diode Silicon Epitaxial Planar Type

# **1SS370**

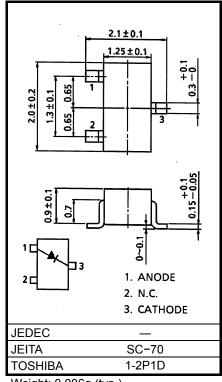
## High Voltage, High Speed Switching Applications

 $\begin{array}{ll} \bullet & \text{Low forward voltage} & \vdots \text{VF (2)} = 0.9 \text{V (typ.)} \\ \bullet & \text{Fast reverse recovery time: } t_{rr} = 60 \text{ns (max)} \\ \bullet & \text{Small total capacitance} & \vdots \text{CT} = 1.5 \text{pF (typ.)} \\ \end{array}$ 

• Small package : SC-70

### Absolute Maximum Ratings (Ta = 25°C)

| Characteristic                 | Symbol           | Rating     | Unit |
|--------------------------------|------------------|------------|------|
| Maximum (peak) reverse voltage | $V_{RM}$         | 250        | V    |
| Reverse voltage                | V <sub>R</sub>   | 200        | V    |
| Maximum (peak) forward current | I <sub>FM</sub>  | 300        | mA   |
| Average forward current        | Io               | 100        | mA   |
| Surge current (10ms)           | I <sub>FSM</sub> | 2          | Α    |
| Power dissipation              | Р                | 100        | mW   |
| Junction temperature           | Tj               | 125        | °C   |
| Storage temperature range      | T <sub>stg</sub> | -55 to 125 | °C   |



Weight: 0.006g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high

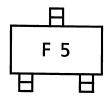
temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

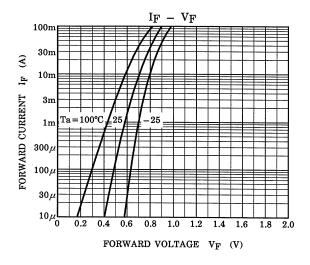
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

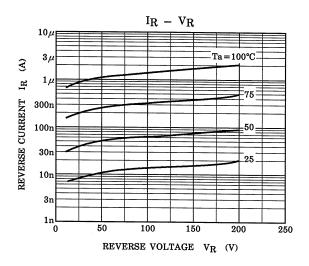
#### **Electrical Characteristics (Ta = 25°C)**

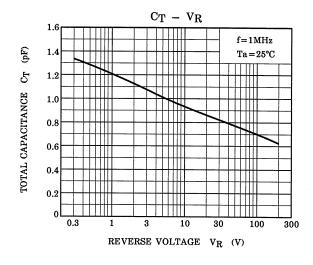
| Characteristic        | Symbol             | Test<br>Circuit | Test Condition                           | Min | Тур. | Max | Unit |
|-----------------------|--------------------|-----------------|--|-----|------|-----|------|
| Forward voltage       | V <sub>F (1)</sub> | _               | I <sub>F</sub> = 10mA                    | ı   | 0.72 | 1.0 | V    |
|                       | V <sub>F (2)</sub> | _               | I <sub>F</sub> = 100mA                   | 1   | 0.90 | 1.2 |      |
| Reverse current       | I <sub>R (1)</sub> | _               | V <sub>R</sub> = 50V                     | -   | _    | 0.1 | μΑ   |
|                       | I <sub>R (2)</sub> | _               | V <sub>R</sub> = 200V                    | _   | _    | 1.0 |      |
| Total capacitance     | C <sub>T</sub>     | _               | V <sub>R</sub> = 0, f = 1MH <sub>z</sub> | _   | 1.5  | 3.0 | pF   |
| Reverse recovery time | t <sub>rr</sub>    | _               | I <sub>F</sub> = 10mA, Fig.1             | -   | 10   | 60  | ns   |

#### Marking









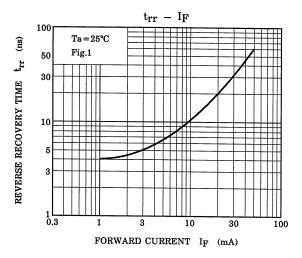
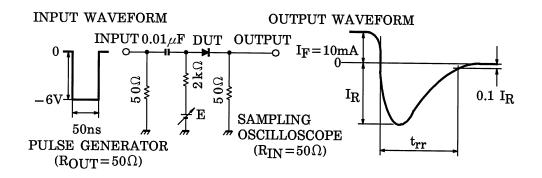


Fig.1 Reverse Recovery Time (trr) Test Circuit



2 2014-03-01

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