Installation Instructions for the Honeywell Humidlcon™ Digital Humidity/Temperature Sensors: HIH6130/6131 Series with SPI Communication

ISSUE 2 50070066

GENERAL INFORMATION

The following associated literature is available on the Web:

- Product data sheet
- · Product application note
- · Application sheets:
 - Humidity Sensor Performance Characteristics
 - Humidity Sensor Theory and Behavior
 - Humidity Sensor Moisture and Psychrometrics
 - Humidity Sensor Chemical Resistivity
 - Thermoset Polymer-based Capacitive Sensors
- · Technical Notes:
 - SPI Communication with the Honeywell HumidIcon™
 Digital Humidity/Temperature Sensors
 - Using Alarms on the Honeywell Humidlcon™ Digital Humidity/Temperature Sensors
 - Entering and Using Command Mode on the Honeywell HumidIcon™ Digital Humidity/Temperature Sensors
 - Total Error Band Specification for Honeywell Digital Humidity/Temperature Sensors

CAUTION

IMPROPER HANDLING

- Do not remove the sensor from its original protective packaging until it is ready to be installed.
- Do not touch the sensor surface. Use latex finger cots.
 Handle the sensor by its package edges or leads.
- Do not allow objects to enter the cavity of the sensor element.
- Do not allow debris or contaminants to accumulate on the filter.

Failure to comply with these instructions may result in product damage.

SOLDERING AND ASSEMBLY

CAUTION

IMPROPER CLEANING

 Insert and solder the sensor after the PCB cleaning process.

Failure to comply with these instructions may result in product damage.

CAUTION

IMPROPER SENSOR POSITIONING

 Position the sensor so that its face is exposed to the atmosphere being monitored.

Failure to comply with these instructions may result in product damage.

CAUTION

IMPROPER SOLDERING

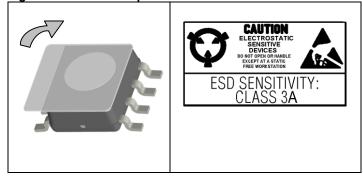
- For better product reliability, the HIH6130/6131 Series is factory-shipped with a protective tape on the cover (sensing face). This tape must remain in place during soldering.
- After soldering, remove the protective tape as described below to activate the sensor.

Failure to comply with these instructions may result in product damage.

PROTECTIVE TAPE REMOVAL

- 1. Use proper ESD protection.
- Using covered fingers or tweezers to ensure that no debris
 falls into the filter, sensor cover or die, grip the
 overhanging front edge of the protective tape and peel it
 back from the top surface of the sensor.
- Ensure the complete rectangle of protective tape is removed.

Figure 1. Protective Tape Removal



SOLDERING

Process

- Automated: Use a no-clean flux. Limit the contact of the flux to the leads only. Reflow soldering profile is specified by J-STD-020D for a small package with a peak temperature of 260 °C [500 °F].
- Manual: Do not exceed 350 °C [662 °F] for 4 s max.

SENSOR REHYDRATION

NOTICE

SENSOR REHYDRATION

- Exposure to elevated temperatures, such as those experienced during solder reflow, may dry out the sensing element. It is recommended that the sensor be allowed time to rehydrate after soldering or other high temperature/dry exposures.
- Exposure to these conditions will not permanently damage the sensor. It will generally return to its factorycalibrated value after rehydration.
- If rehydration is not performed, the sensor may read a slight offset that slowly disappears over time as the sensor becomes exposed to ambient conditions.
 Conversely, extended exposure to condensing and high humidity environments (>90 %RH) may cause a revisable shift in readings which will generally return to normal after the sensor has been allowed to dry off.

To rehydrate the sensor, expose it to room temperature under ambient conditions (>50 %RH) for a minimum of five hours.

MOISTURE SEALING THE LEADS

If, in the presence of intermittent moisture or other contaminants, there is the possibility of galvanic paths between the leads, moisture seal the leads and exposed pads.

Table 1. Environmental Specifications

Characteristic	Condition	Min.	Тур.	Max.	Unit
Operating temperature range	_	-25 [-13]	_	85 [185]	°C [°F]
Storage temperature range	_	-40 [-40]	_	85 [185]	°C [°F]
Storage humidity	-	30	_	50	%RH
Soldering:					
automated	IPC/EIA/JEDEC J-STD-020D	_	_	260 [500]	°C [°F]
manual	apply heat for 4 s max. for manual soldering	_	_	350 [662]	
ESD	MIL-STD 883H, Method 3015.7	_	-	±4	kV
Latch-up immunity	_	_	_	±100	mA
Shock	MIL-STD 202G, Method 213D, Test Condition C, half-sine, 6 ms ±3 perpendicular axis, 3 shock	-	_	100	g
	pulses per axis				9
Vibration	MIL-STD 202G, Method 204D, Test Condition D, 10 Hz to 2000 Hz	-	-	20	g
Light sensitivity	exposed to 50 lumens yellow light;	exhibited no	change in	output	

Table 2. Humidity Performance Specifications

Characteristic	Condition	Min.	Тур.	Max.	Unit
Supply voltage variation	2.3 Vdc to 5.5 Vdc	_	0.1	0.5	%RH
Compensated humidity range ¹	-	10	_	90	%RH
Compensated temp. range ¹	-	5 [41]	_	50 [122]	°C [°F]
Resolution	14 bit ADC resolution	_	_	0.04	%RH
Accuracy ²	-	-	_	±4	%RH
Total error band ³	-	-	_	±5	%RH
Response time	airflow minimum 20 l/min	_	6	8	S
Operating range	non-condensing	0	_	100	%RH
Long term stability	50 %RH for 5 years	-	±0.05	±1.2	%RH
Impact of soldering	IPC/EIA/JEDEC J-STD-020D, peak temp. of 260 °C [500 °F]	_	-	±2.5	%RH

Notes:

1. Conversion formulas:

Conversion formulas.	
14 bit ADC output for humidity to %RH:	14 bit ADC output for temperature to %RH:
$Humidity(\%RH) = \frac{Humidity_14_bit_ADC_output}{2^{14}-2} \times 100$	Temperatue(°C) = $\frac{\text{Temperatue}_14_\text{bit}_\text{ADC}_\text{output}}{2^{14}-2} \times 165-40$

- 2. Accuracy is specified at the typical supply voltage of 3.3 Vdc and at 25 °C [77 °F]. It is the maximum deviation from the ideal transfer function of relative humidity measured over the humidity range of 10 %RH to 90 %RH and includes all errors due to humidity non-linearity, humidity hysteresis and humidity non-repeatability.
- 3. Total error band is the maximum deviation from the ideal transfer function of relative humidity over the compensated range of 5 °C [41 °F] to 50 °C [122 °F]. It includes all errors due to humidity non-linearity, humidity hysteresis, humidity non-repeatability, thermal effect on zero, thermal effect on span and thermal hysteresis.

Table 3. Temperature Performance Specifications

Characteristic	Condition	Min.	Тур.	Max.	Unit
Supply voltage variation	2.3 Vdc to 5.5 Vdc	_	0.5	1.0	°C
Compensated temp. range	_	5 [41]	_	50 [122]	°C [°F]
Resolution	14 bit ADC resolution	_	_	0.025	°C
Accuracy (BFSL) ¹	_	-	_	±1.0	°C
Response time	1/e slow moving air	5	-	30	s
Long term stability	25 °C for 5 years	_	_	±0.05	°C/yr
Impact of soldering	IPC/EIA/JEDEC J-STD-020D, peak temp.	_	_	±0.1	°C
	of 260 °C [500 °F]				

Note:

Table 4. Current Consumption

Characteristic	VDD	Abbr.	Condition	Тур.	Max.	Unit
Sleep current	3.3	ISLEEP	_	0.6	1	μA
Supply current	3.3	IDD	14 bit fastest update, no sleep	0.75	1	mA

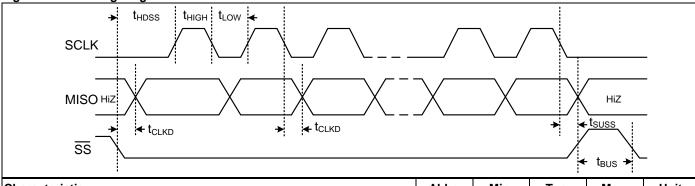
Table 5. Input and Output Characteristics

Characteristic	Abbr.	Condition	Min.	Тур.	Max.	Unit
Supply voltage	V _{DD}	_	2.3	3.3	5.5	Vdc
Low level output voltage	Vol	IoL = 2.8 mA min.	-	_	20%	VDD
High level output voltage	Vон	Iон = -2.8 mA min.	80%		_	VDD
Low level input voltage	VIL	_	_	_	20%	VDD
High level input voltage	VIH	-	80%		_	VDD
SPI pull-up resistor (for SS only)	Rss	<u>-</u>	_	10	_	kOhm

Table 6. Measurement Timing

Table of Meacarement Timing						
Characteristic	Abbr.	Condition	Min.	Тур.	Max.	Unit
Start-up time (Power-On to data	TSTA	14 bit temperature and 14 bit humidity	_	50	60	ms
ready)		resolution				
Update rate	app	lication dependent: measurements are tak	en only whe	n the applic	ation reque	sts them

Figure 2. SPI Timing Diagram



Characteristic	Abbr.	Min.	Тур.	Max.	Unit
SCLK clock frequency	f _{SCL}	50	_	800	kHz
SS drop to first clock edge	t _{HDSS}	2.5	_	-	μs
Minimum SCLK clock low width ¹	t _{LOW}	0.6	_	_	μs
Minimum SCLK clock high width ¹	t _{HIGH}	0.6	_	_	μs
Clock edge to data transition	t _{CLKD}	0	-	0.5	μs
Rise of SS relative to last clock edge	t _{SUSS}	0.1	_	_	μs
Bus free time between rise and fall of SS	t _{BUS}	2	_	_	μs

Note 1: Combined low and high widths must equal or exceed minimum SCLK period.

^{1.} Accuracy is specified over the compensated temperature range.

Figure 3. Mounting Dimensions, PCB Landing Pattern and Pinout (For reference only. mm/[in]) 1,905 L 4.90 8X 0,61 [0.193][0.075] [0.024]0,15 [0.006]8X 2,03 [0.08]3,90 6.0 [0.153][0.236]4X 4,9 [0.193] 0,203 [0.0080] 1,27 0,41 2,055 [0.050][0.016][0.081] 6X 1,27 [0.05]**ASIC Pad** Pin Description 1 connect via 0.1 µF to ground VCORE 2 Vss supply ground 3 SS slave select (input) **SCLK** 4 serial clock 5 MISO master-in-slave-out

alarm output low

not connected externally supply voltage, connect via 0.22 µF to ground

Note:

6¹

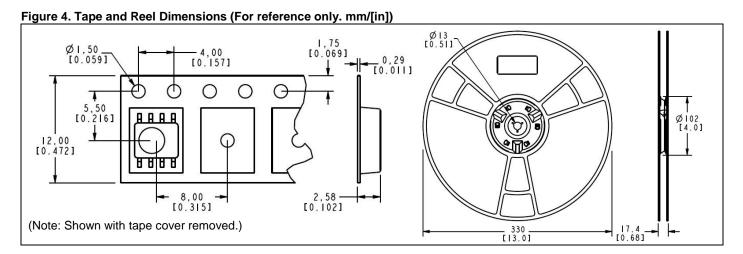
7

8

AL_L

NC

 V_{DD}



^{1.} Do not connect Pin 6 if the built-in alarm feature is not desired.

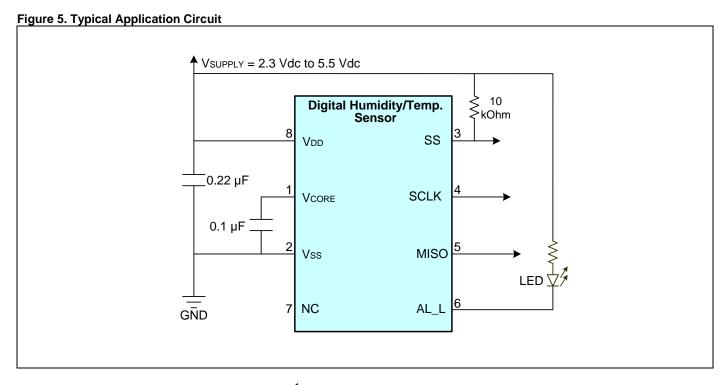


Figure 6. HIH6130/6131 Series Maximal Accuracy	(Performance is specified between 10 %RH and 90 %RH.)

		_	_			_	_	_	_	_
90	±1	±2	±2	±2	±3	±3	±3	±3	±3	±2
	±1	±2	±2	±2	±3	±3	±3	±3	±3	±2
80	±1	±2	±2	±2	±2	±2	±2	±2	±2	±1
	±2	±2	±2	±2	±2	±2	±2	±2	±2	±2
70	±2	±3	±3	±3	±2	±2	±2	±2	±2	±2
	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
60	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
50	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
60 50 40 30	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
40	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
30	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
	±2	±3	±3	±3	±3	±3	±3	±3	±3	±2
20	±1	±3	±3	±3	±3	±3	±3	±3	±3	±1
	±1	±3	±3	±3	±3	±3	±3	±3	±3	±1
10	±1	±3	±3	±3	±3	±3	±3	±3	±3	±1
0		10	•	20	•	30		40	•	50

Note:

Maximal accuracy is the mean value between rising and falling RH points.

A WARNING

PERSONAL INJURY

- DO NOT USE these products as safety or emergency stop devices or in any other application where failure of the product could result in personal injury.
- The sensor's housing does not provide electrical safety isolation. Mount the sensor in a location where it cannot be touched, or where a surrounding barrier prevents human contact.

Failure to comply with these instructions could result in death or serious injury.

WARRANTY/REMEDY

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