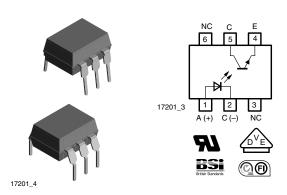
### Vishay Semiconductors



## **Optocoupler, Phototransistor Output**



#### DESCRIPTION

The TCDT1100, TCDT1100G series consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 6 pin plastic dual inline package. The base of the phototransistor is not connected providing noise immunity.

#### **VDE STANDARDS**

These couplers perform safety functions according to the following equipment standards:

- DIN EN 60747-5-5 (VDE0884)
   Optocoupler for electrical safety requirements
- IEC 60950/EN 60950
   Office machines (applied for reinforced isolation for mains voltage ≤ 400 V<sub>RMS</sub>)
- VDE0804
   Telecommunication apparatus and data processing
- IEC 60065
   Safety for mains-operated electronic and related household apparatus

#### **FEATURES**

- Isolation test voltage 5000 V<sub>RMS</sub>
- High common mode rejection
- No base terminal connection for improved noise immunity



- CTR offered in 4 groups
- Thickness though insulation ≥ 0.4 mm
- Creepage current resistance according to VDE0303/ IEC 60112 comparative tracking index: CTI ≥ 275
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC

#### **APPLICATIONS**

- Switch-mode power supplies
- · Line receiver
- · Computer peripheral interface
- · Microprocessor system interface
- Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):
  - for appl. class I IV at mains voltage ≤ 300 V
  - for appl. class I III at mains voltage  $\leq$  600 V according to DIN EN 60747-5-5

#### **AGENCY APPROVALS**

- UL1577, file no. E52744, double protection
- BSI IEC 60950; IEC 60065 pending
- DIN EN 60747-5-5 (VDE0884)
- FIMKO

ORDER INFORMATION	
PART	REMARKS
TCDT1100	CTR > 40 %, DIP-6
TCDT1101	CTR 40 % to 80 %, DIP-6
TCDT1102	CTR 63 % to 125 %, DIP-6
TCDT1103	CTR 100 % to 200 %, DIP-6
TCDT1100G	CTR > 40 %, DIP-6, 400 mil
TCDT1101G	CTR 40 % to 80 %, DIP-6, 400 mil
TCDT1102G	CTR 63 % to 125 %, DIP-6, 400 mil
TCDT1103G	CTR 100 % to 200 %, DIP-6, 400 mil

#### Note

• G = leadform 10.16 mm; G is not marked on the body.



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<b>ABSOLUTE MAXIMUM RATINGS <sup>(1)</sup></b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT	<u> </u>						
Reverse voltage		V <sub>R</sub>	5	V			
Forward current		I <sub>F</sub>	60	mA			
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	3	Α			
Power dissipation		P <sub>diss</sub>	70	mW			
Junction temperature		T <sub>j</sub>	125	°C			
OUTPUT			<u>.                                      </u>				
Collector emitter voltage		$V_{CEO}$	32	V			
Emitter collector voltage		$V_{ECO}$	7	V			
Collector current		I <sub>C</sub>	50	mA			
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA			
Power dissipation		P <sub>diss</sub>	70	mW			
Junction temperature		Tj	125	°C			
COUPLER							
Isolation test voltage (RMS)		V <sub>ISO</sub>	5000	V <sub>RMS</sub>			
Total power dissipation		P <sub>tot</sub>	200	mW			
Ambient temperature range		T <sub>amb</sub>	- 55 to + 110	°C			
Storage temperature range		T <sub>stg</sub>	- 55 to + 125	°C			
Soldering temperature (2)	2 mm from case, t ≤ 10 s	T <sub>sld</sub>	260	°C			

#### Notes

<sup>(2)</sup> Refer to wave profile for soldering conditions for through hole devices.

<b>ELECTRICAL CHARACTERISTCS</b> (1) (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT	INPUT							
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1.25	1.6	V		
Junction capacitance	$V_R = 0$ , $f = 1$ MHz	C <sub>j</sub>		50		pF		
OUTPUT								
Collector emitter voltage	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	32			V		
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V		
Collector ermitter cut-off current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I <sub>CEO</sub>		200		nA		
COUPLER								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V <sub>CEsat</sub>			0.3	V		
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>		110		kHz		
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.6		pF		

#### Note

<sup>(1)</sup> Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Minimum and maximum values are testing requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

# Vishay Semiconductors Optocoupler, Phototransistor Output



CURRENT TRANSFER RATIO								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
		TCDT1100	CTR	40			%	
		TCDT1100G	CTR 40			%		
l <sub>O</sub> /l <sub>F</sub>		TCDT1101	CTR	40		80	%	
	V - 5 V I - 10 mA	TCDT1101G	CTR			80	%	
	$V_{CE} = 5 \text{ V}, I_{F} = 10 \text{ mA}$	TCDT1102	CTR	63	125	105	%	
		TCDT1102G	CTR			123	%	
		TCDT1103	CTR	100		200	%	
		TCDT1103G	CTR			200	%	

MAXIMUM SAFETY RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward current		I <sub>F</sub>			130	mA		
OUTPUT								
Power dissipation		P <sub>diss</sub>			265	mW		
COUPLER								
Rated impulse voltage		V <sub>IOTM</sub>			6	kV		
Safety temperature		T <sub>si</sub>			150	°C		

#### Note

 According to DIN EN 60747-5-5. This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

INSULATION RATED PARAMETERS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	$V_{pd}$	1.6			kV	
Partial discharge test voltage -	t <sub>Tr</sub> = 60 s, t <sub>test</sub> = 10 s, (see figure 1)	$V_{IOTM}$	6			kV	
lot test (sample test)		$V_{pd}$	1.3			kV	
Insulation resistance	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>			Ω	
	$V_{IO} = 500 \text{ V}, T_{amb} = 100 ^{\circ}\text{C}$	R <sub>IO</sub>	10 <sup>11</sup>			Ω	
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>			Ω	

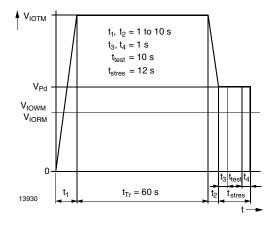


Fig. 1 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-5/DIN EN 60747-; IEC60747



# Optocoupler, Phototransistor Output Vishay Semiconductors

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega$ , (see figure 2)	t <sub>d</sub>		4		μs	
Rise time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see figure 2)}$	t <sub>r</sub>		7		μs	
Fall time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see figure 2)}$	t <sub>f</sub>		6.7		μs	
Storage time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see figure 2)}$	t <sub>s</sub>		0.3		μs	
Turn-on time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see figure 2)}$	t <sub>on</sub>		11		μs	
Turn-off time	$V_S = 5 \text{ V}, I_C = 5 \text{ mA}, R_L = 100 \Omega, \text{ (see figure 2)}$	t <sub>off</sub>		7		μs	
Turn-on time	$V_S = 5 \text{ V}, I_C = 10 \text{ mA}, R_L = 1 \text{ k}\Omega, \text{ (see figure 3)}$	t <sub>on</sub>		25		μs	
Turn-off time	$V_S = 5 \text{ V}, I_C = 10 \text{ mA}, R_L = 1 \text{ k}\Omega, \text{ (see figure 3)}$	t <sub>off</sub>		42.5		μs	

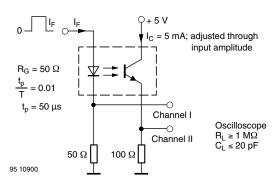


Fig. 2 - Test Circuit, Non-Saturated Operation

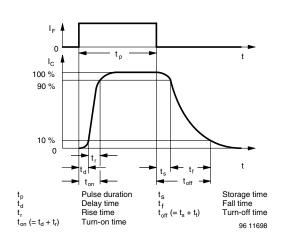


Fig. 4 - Switching Times

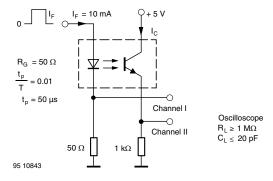


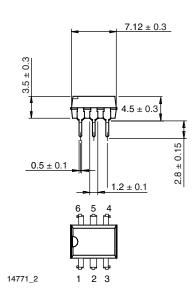
Fig. 3 - Test Circuit, Saturated Operation

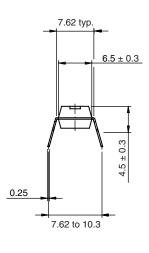
# Vishay Semiconductors Optocoupler, Phototransistor Output



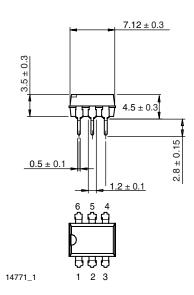
### **PACKAGE DIMENSIONS** in millimeters

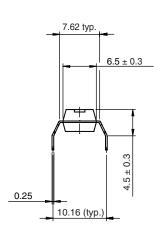
DIP-6



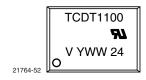


DIP-6, 400 mil





### **PACKAGE MARKING**





Vishay

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